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LIST OF PUBLICATIONS CITED BY APPLICANT	Atty. Docket No. SEL 125	Serial No. 09/255,605
	Applicant Shunpei YAMAZAKI et al	
	Filing Date February 22, 1999	Group 2629

US PATENT DOCUMENTS

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(Include name of author (in CAPITAL LETTERS), title of article or item (book, magazine, journal, serial, symposium, catalog, etc.) date, pages(s), volume-issue number(s), publisher, city and/or country where published).

VLS	1) SHINOHARA, T. et al, "High-Performance Polycrystalline Silicon TFTs Using Self-Aligned Grain Boundary Control Technique," Electronics and Communications in Japan, Part II: Electronics, vol. 76, no. 10, pp. 99-106, (October, 1993).

EXAMINER: Vincent E. Kovalick	DATE CONSIDERED: 2/20/07

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